NATIONAL BOARD OF ACCREDITATION

Data Capturing Points of the Program Applied for NBA Accreditation– Tier I/II UG (Engineering) Institute Programs

Program Name : Electronics & Communication Engineering	**Discipline**: Engineering & Technology
Level : Under Graduate	**Tier**: 2
Application No: 11426	**Date of Submission**: 24-01-2026

PART A- Profile of the Institute

A1.Name of the Institute: SANTHIRAM ENGINEERING COLLEGE	
Year of Establishment : 2007	Location of the Institute: NANDYAL
A2. Institute Address:NH-18, NANDYAL-518501, KURNOOL DIST, ANDHRA PRADESH, INDIA	
City:Kurnool	State:Andhra Pradesh
Pin Code:518501	Website:www.srecnandyal.edu.in
Email:principal@srecnandyal.edu.in	Phone No(with STD Code):-
A3. Name and Address of the Affiliating University (if any):	
Name of the University : JNTUA UNIVERSITY	City:
State : Andhra Pradesh	Pin Code: 0
A4. Type of the Institution: Self-Supported Institute	
A5. Ownership Status: Self financing	

A6. Details of all Programs being Offered by the Institution:

- No. of UG programs: **6**
- No. of PG programs: **6**

Table No. A6.1: List of all programs offered by the Institute.

Sr.No.	Discipline	Level of program	Name of the program	Year of Start	Year of Closed	Name of The Department
1	Computer Application	PG	Master in Computer Applications	2023	--	Computer Application
2	Engineering & Technology	UG	Computer Science and Design	2021	2023	Computer Science and Design
3	Engineering & Technology	PG	Computer Science and Engineering	2024	--	Computer Science and Engineering
4	Engineering & Technology	UG	Computer Science and Engineering	2007	--	Computer Science and Engineering
5	Engineering & Technology	UG	Computer Science and Engineering (Artificial Intelligence & Machine Learning)	2023	--	Computer Science and Engineering (Artificial Intelligence and Machine Learning)
6	Engineering & Technology	PG	Computer Science and Engineering (Artificial Intelligence and Machine Learning)	2024	--	Computer Science and Engineering
7	Engineering & Technology	UG	Computer Science and Engineering (Data Science)	2020	--	Computer Science and Engineering (Data Science)

8	Engineering & Technology	UG	Electrical & Electronics Engineering	2007	--	Electrical and Electronics Engineering
9	Engineering & Technology	UG	Electronics & Communication Engineering	2007	--	Electronics and Communication Engineering
10	Engineering & Technology	PG	Embedded Systems	2024	--	Electronics and Communication Engineering
11	Engineering & Technology	PG	VLSI System Design	2011	--	Electronics and Communication Engineering
12	Management	PG	Master of Business Administration	2009	--	Management

A7. Programs to be considered for Accreditation vide this Application:

Table No. A7.1: List of programs to be considered for accreditation.

Name of the Department	Having Allied Departments	Name of the Program	Program Level
Computer Science and Engineering	Yes	Computer Science and Engineering	UG
Electronics and Communication Engineering	No	Electronics & Communication Engineering	UG

Table No. A7.2: Allied Department(s) to the Department of the program considered for accreditation as above.
Cluster ID. Name of the Department (in table no. A7.1) Name of allied Departments/Cluster (for table no. A7.1)

No Record

PART-B: Program information

B1. Provide the Required Information for the Program Applied For:

Table No. B1: Program details.
A. List of the Programs Offered by the Department:

SR.NO.	PROGRAM NAME	PROGRAM APPLIED LEVEL	YEAR OF START / YEAR OF CLOSED	SANCTIONED INTAKE	INCREASE/DECREASE INTAKE (if any)	YEAR OF INCREASE/DECREASE	CURRENT INTAKE	YEAR OF AICTE APPROVAL	AICTE/COMPETENT AUTHORITY ARROVAL DETAILS	ACCREDITATION STATUS	FROM	TO	NO. OF TIMES PROGRAM ACCREDITED	PROGR DURAT
1	Electronics & Communication Engineering	UG	2007 / --	60	Yes	2025	180	2025	F.No.South-Central/1-44643684683/2025/EOA	Granted accreditation for 3 years for the period (specify period)	2023	2026	1	4

Sanctioned Intake for Last Five Years for the Embedded Systems	
Academic Year	**Sanctioned Intake**
2025-26	180
2024-25	120
2023-24	120
2022-23	120
2021-22	120
2020-21	120

List of the Allied Departments/Cluster and Programs:

B2. Detail of Head of the Department for the program under consideration:

A. Name of the HoD :	Dr Y MALLIKARJUNA RAO
B. Nature of appointment:	Regular
C. Qualification:	M.Tech and Ph.D.

B3. Program Details

Table No.B3.1: Admission details for the program excluding those admitted through multiple entry and exit points.

Item (Information to be provided cumulatively for all the shifts with explicit headings, wherever applicable)	2025-26 (CAY)	2024-25 (CAYm1)	2023-24 (CAYm2)	2022-23 (CAYm3)	2021-22 (CAYm4)	2020-21 (CAYm5)	2019-20 (CAYm6)
N=Sanctioned intake of the program (as per AICTE /Competent authority)	180	120	120	120	120	120	120
N1=Total no. of students admitted in the 1st year minus the no. of students, who migrated to other programs/ institutions plus no. of students, who migrated to this program	180	120	120	98	109	120	120
N2=Number of students admitted in 2nd year in the same batch via lateral entry including leftover seats	0	16	12	18	12	12	12
N3=Separate division if any	0	0	0	0	0	0	0
N4=Total no. of students admitted in the 1st year via all supernumerary quotas	16	12	12	3	11	7	9
Total number of students admitted in the program (N1 + N2 + N3 + N4) - excluding those admitted through multiple entry and exit points.	196	148	144	119	132	139	141

CAY= Current Academic Year. CAYm1= Current Academic Year Minus 1 CAYm2= Current Academic Year Minus 2. LYG= Last Year Graduate. LYGm1= Last Year Graduate Minus 1. LYGm2= Last Year Graduate Minus 2.

B4. Enrolment Ratio in the First Year

Table No. B4.1: Student enrolment ratio in the 1st year.

Year of entry	N (From Table 4.1)	N1 (From Table 4.1)	N4 (From Table 4.1)	Enrollment Ratio [(N1/N)*100]
2025-26 (CAY)	180	180	16	108.89
2024-25 (CAYm1)	120	120	12	110.00
2023-24 (CAYm2)	120	120	12	110.00

Average [(ER1 + ER2 + ER3) / 3] = 109.63≡ 100

B5. Success Rate of the Students in the Stipulated Period of the Program

Table No.B5.1: The success rate in the stipulated period of a program.

Item	(2021-22) LYG	(2020-21) LYGm1	(2019-20) LYGm2
A*= (No. of students admitted in the 1st year of that batch and those actually admitted in the 2nd year via lateral entry, plus the number of students admitted through multiple entry (if any) and separate division if applicable, minus the number of students who exited through multiple entry (if any).	132.00	139.00	141.00
B=No. of students who graduated from the program in the stipulated course duration	83.00	82.00	97.00

Success Rate (SR)= (B/A) * 100	62.88	58.99	68.79

Average SR of three batches ((SR_1+ SR_2+ SR_3)/3): 63.55

B6. Academic Performance of the First-Year Students of the Program

Table No.B6.1: Academic Performance of the First-Year Students of the Program.

Academic Performance	CAYm1(2024-25)	CAYm2(2023-24)	CAYm3 (2022-23)
X=(Mean of 1st year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 1st year/10)	6.94	7.12	6.63
Y=Total no. of successful students	123.00	131.00	99.00
Z=Total no. of students appeared in the examination	132.00	132.00	101.00
API [X*(Y/Z)]	6.47	7.07	6.50

Average API[(AP1+AP2+AP3)/3] : 6.68

B7: Academic Performance of the Second Year Students of the Program

Table No.B7.1: Academic Performance of the Second Year Students of the Program.

Academic Performance	CAYm1 (2024-25)	CAYm2 (2023-24)	CAYm3 (2022-23)
X=(Mean of 2nd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 2rd year/10)	6.88	6.25	6.22
Y=Total no. of successful students	139.00	115.00	115.00
Z=Total no. of students appeared in the examination	143.00	117.00	118.00
API [X * (Y/Z)]	6.69	6.14	6.06

Average API [(AP1 + AP2 + AP3)/3] : 6.30

B8. Academic Performance of the Third Year Students of the Program

Table No.B8.1: Academic Performance of the Third Year Students of the Program

Academic Performance	CAYm1 (2024-25)	CAYm2 (2023-24)	CAYm3 (2022-23)
X=(Mean of 3rd year grade point average of all successful students on a 10-point scale) or (Mean of the percentage of marks of all successful students in 3rd year/10)	6.81	6.83	6.93
Y=Total no. of successful students	111.00	113.00	126.00
Z=Total no. of students appeared in the examination	115.00	115.00	127.00
API [X*(Y/Z)]:	6.57	6.71	6.88

Average API [(AP1 + AP2 + AP3)/3] : 6.72

B9. Placement, Higher Studies, and Entrepreneurship

Table No.B9.1: Placement, higher studies, and entrepreneurship details.

Item	LYG (2021-22)	LYGm1(2020-21)	LYGm2(2019-20)
FS*=Total no. of final year students	132.00	132.00	135.00
X=No. of students placed	89.00	85.00	98.00
Y=No. of students admitted to higher studies	13.00	20.00	9.00
Z= No. of students taking up entrepreneurship	0.00	0.00	0.00
Placement Index(P) = (((X + Y + Z)/FS) * 100):	77.27	79.55	79.26

Average Placement Index = (P_1 + P_2 + P_3)/3: 78.69 Placement Index Points:

PART C: Faculty Details in Department and Allied Departments

(Data to be filled in for the Department and Allied Departments)

C1. Faculty details of Department and Allied Departments

Table No.C1: Faculty details in the Department for the past 3 years including CAY

Sr.No	Name of the Faculty	PAN No.	Highest degree	University	Area of Specialization	Date of Joining in this Institution	Experience in years in current institute	Designation at Time Joining in this Institution	Present Designation	The date on which Designated as Professor/ Associate Professor if any	Nature of Association (Regular/ Contract/ Ad hoc)	Currently Associated (Y/N)	In case of NO, Date of Leaving	IS HOD?
1	Dr M V SUBRAMANYAM	XXXXXXX30P	M.Tech and Ph.D.	Jawaharlal Nehru Technological University ,Hyderabad	Wireless Networks	15/06/2007	18.7	Professor	Professor	15/06/2007	Regular	Yes		No
2	Dr Y MALLIKARJUNA RAO	XXXXXXX54P	M.Tech and Ph.D.	Jawaharlal Nehru Technological University Kakinada	Wireless Communications	20/08/2007	18.5	Assistant Professor	Professor	01/07/2020	Regular	Yes		Yes
3	Dr G SOWMYA	XXXXXXX30K	M.Tech and Ph.D.	K L University	Wireless Networks	11/02/2013	12.11	Assistant Professor	Associate Professor	01/11/2022	Regular	Yes		No
4	Dr NAYAKANTI PRAVEEN KUMAR	XXXXXXX68K	M.Tech and Ph.D.	Jawaharlal Nehru Technological University Ananthapuramu	Low Power VLSI	01/06/2024	1.7	Associate Professor	Associate Professor	01/06/2024	Regular	Yes		No
5	Dr K KAMESHWARA REDDY	XXXXXXX99N	M.Tech and Ph.D.	Anna University	VLSI	08/07/2019	5.10	Professor	Professor	08/07/2019	Regular	No	26/05/2025	No
6	Dr BADUGU DIVYA MADHURI	XXXXXXX87D	M.Tech and Ph.D.	Koneru Lakshmaiah Education Foundation	VLSI	25/07/2022	3.6	Associate Professor	Associate Professor	25/07/2022	Regular	Yes		No
7	Dr UDAYABHASKAR PATTAPU	XXXXXXX88J	M.Tech and Ph.D.	IIT Dhanbad	ANTENNAS	05/08/2020	4.9	Assistant Professor	Associate Professor	20/10/2020	Regular	No	26/05/2025	No
8	Dr SHIV NATH CHAUDHRI	XXXXXXX38H	M.Tech and Ph.D.	IIT-BHU	SIGNAL PROCESSING	01/02/2023	2.3	Assistant Professor	Assistant Professor		Regular	No	26/05/2025	No

9	Dr SHAIK JAVID BASHA	XXXXXXX15L	M.Tech and Ph.D.	Jawaharlal Nehru Technological University Anantapur	VLSI and Nano electronics	24/08/2023	2.5	Assistant Professor	Assistant Professor		Regular	Yes		No
10	Dr KATEPOGU RAJKUMAR	XXXXXXX31E	M.Tech and Ph.D.	SRI VENKATESWARA UNIVERSITY, TIRUPATI	VLSI, Micro and Nano electronics	01/08/2024	1.5	Assistant Professor	Assistant Professor		Regular	Yes		No
11	Dr PALUKURI SURENDRA BABU	XXXXXXX20G	M.Tech and Ph.D.	Sri Satya Sai University of Technology and Medical Sciences, Bhopal	Low Power VLSI	13/02/2023	2.11	Assistant Professor	Associate Professor	05/11/2025	Regular	Yes		No
12	SANIVARAPU RAMBABU	XXXXXXX57J	M.Tech	Jawaharlal Nehru Technological University Anantapur	VLSI	08/08/2011	14.5	Assistant Professor	Assistant Professor		Regular	Yes		No
13	SREENIVASA RAO N	XXXXXXX07Q	M.Tech	Jawaharlal Nehru Technological University Anantapur	EMBEDDED SYSTEMS	21/06/2011	14.6	Assistant Professor	Assistant Professor		Regular	Yes		No
14	SYED MUNAWWAR	XXXXXXX10B	M.Tech	Jawaharlal Nehru Technological University ,Hyderabad	EMBEDDED SYSTEMS	22/05/2012	13.8	Assistant Professor	Assistant Professor		Regular	Yes		No
15	BIKKILI ALEKYA HIMABINDU	XXXXXXX97F	M.Tech	Jawaharlal Nehru Technological University ,Anatapur	DE&CS	01/07/2015	10.6	Assistant Professor	Assistant Professor		Regular	Yes		No
16	VADALA NAGAMANI	XXXXXXX12J	M.Tech	Jawaharlal Nehru Technological University ,Kakinada	EMBEDDED SYSTEMS	18/06/2018	7.7	Assistant Professor	Assistant Professor		Regular	Yes		No
17	M MAHESH KUMAR	XXXXXXX22R	M.Tech	Jawaharlal Nehru Technological University ,Hyderabad	Digital Systems & Computer Electronics	10/07/2019	6.6	Assistant Professor	Assistant Professor		Regular	Yes		No
18	GIRISH SANKARAYOGI	XXXXXXX17C	M.Tech	Jawaharlal Nehru Technological University ,Anatapur	VLSI SYSTEM DESIGN	03/07/2019	6.6	Assistant Professor	Assistant Professor		Regular	Yes		No
19	JAYA MANGALA SRISTI	XXXXXXX03D	M.Tech	Jawaharlal Nehru Technological University ,Hyderabad	VLSI SYSTEM DESIGN	10/07/2019	6.6	Assistant Professor	Assistant Professor		Regular	Yes		No

20	M ANIL KUMAR	XXXXXXX24L	M.Tech	Jawaharlal Nehru Technological University, Anantapur	Communication and Signal Processing	13/08/2020	5.4	Assistant Professor	Assistant Professor		Regular	Yes		No
21	NAGELLA JYOTHSNA	XXXXXXX71J	M.Tech	Jawaharlal Nehru Technological University ,Anatapur	VLSI System Design	05/07/2021	2.11	Assistant Professor	Assistant Professor		Regular	No	15/06/2024	No
22	SATTI RANGASWAMY	XXXXXXX44C	M.Tech	Jawaharlal Nehru Technological University Anantapur	VLSI	10/07/2019	4.10	Assistant Professor	Assistant Professor		Regular	No	20/05/2024	No
23	K PEDDAOBULESU	XXXXXXX86R	M.Tech	Jawaharlal Nehru Technological University Anantapur	VLSI System Design	15/07/2019	6.5	Assistant Professor	Assistant Professor		Regular	Yes		No
24	DUDEKULA IMRAN	XXXXXXX79E	M.Tech	Jawaharlal Nehru Technological University Anantapur	VLSI System Design	01/08/2023	2.5	Assistant Professor	Assistant Professor		Regular	Yes		No
25	PARADESI MARYSWETHA	XXXXXXX39G	M.Tech	Jawaharlal Nehru Technological University Anantapur	Digital Electronics & Communication Systems	05/07/2021	4.6	Assistant Professor	Assistant Professor		Regular	Yes		No
26	K VINOD KUMAR	XXXXXXX38M	M.Tech	Jawaharlal Nehru Technological University Anantapur	VLSI System Design	01/08/2022	3.5	Assistant Professor	Assistant Professor		Regular	Yes		No
27	SHAIK MOHAMMAD ELIYAS	XXXXXXX28H	M.Tech	SRI VENKATESWARA UNIVERSITY, TIRUPATI	Communication Systems	14/08/2023	2.4	Assistant Professor	Assistant Professor		Regular	Yes		No
28	VEMUGADDA SANKARNATH	XXXXXXX19F	M.Tech	Jawaharlal Nehru Technological University Anantapur	EMBEDDED SYSTEMS	23/08/2023	2.4	Assistant Professor	Assistant Professor		Regular	Yes		No
29	K ABDUL RAHMAN	XXXXXXX58J	M.Tech	Jawaharlal Nehru Technological University Anantapur	VLSI System Design	01/08/2024	1.5		Assistant Professor		Regular	Yes		No
30	NEELIMA GATIKAPOGU	XXXXXXX38Q	M.Tech	Jawaharlal Nehru Technological University Anantapur	VLSI System Design	01/08/2024	1.5	Assistant Professor	Assistant Professor		Regular	Yes		No

Table No.C2: Faculty details of Allied Departments for the past 3 years including CAY.

C2. Student-Faculty Ratio (SFR)

No. of UG(Engineering) programs in Department including allied departments/ clusters (UGn):
UG1=1st UG program
UGn=nth UG program
B= No. of Students in UG 2nd year (ST)
C= No. of Students in UG 3rd year (ST)
D= No. of Students in UG 4th year (ST)
No. of PG (Engineering) programs in Department including allied departments/ clusters (PGm):
PG1=1st PG program.
PGm=mth PG program
A= No. of Students in PG 1st year
B= No. of Students in PG 2nd year
Student Faculty Ratio **(SFR)** = S/F
S= No. of students of all programs in the Department including all students of allied departments/clusters.
No. of students (ST)=Sanctioned Intake (SA)+ Actual admitted students via lateral entry including leftover seats (L) if any (limited to 10 % of SA)
Students who admitted under supernumerary quotas (SNQ, EWS, etc) will not be considered in calculating SFR value. Those students are exempted.
F=Total no. of regular or contractual faculty members (Full Time) in the Department, including allied departments/clusters (excluding first year faculty (The faculty members who have a 100% teaching load in the first-year courses)).

No. of UG Programs in the Department1 No. of PG Programs in the Department2

Table No.C2.1: Student-faculty ratio.

Description	**CAY(2025-26)**	**CAYm1 (2024-25)**	**CAYm2 (2023-24)**
UG1.B	132	132	132
UG1.C	132	132	132
UG1.D	132	132	132
UG1: Electronics & Communication Engineering	**396**	**396**	**396**
PG1.A	18	18	0
PG1.B	18	0	0
PG1: Embedded Systems	**36**	**18**	**0**
PG2.A	18	18	18
PG2.B	18	18	18
PG2: VLSI System Design	**36**	**36**	**36**
DS=Total no. of students in all UG and PG programs in the Department	468	450	432
AS=Total no. of students of all UG and PG programs in allied departments	0	0	0
S=Total no. of students in the Department (DS) and allied departments (AS)	**S1=** 468	**S2=** 450	**S3=** 432
DF=Total no. of faculty members in the Department	25	28	26
AF= Total no. of faculty members in the allied Departments	0	0	0
F=Total no. of faculty members in the Department (DF) and allied Departments (AF)	**F1=** 25	**F2=** 28	**F3=** 26
FF=The faculty members in F who have a 100% teaching load in the first-year courses	0	0	0
Student Faculty Ratio (SFR)=S/(F-FF)	**SFR1= 18.72**	**SFR2= 16.07**	**SFR3= 16.62**
Average SFR for 3 years	**SFR= 17.14**		

C3. Faculty Qualification

- Faculty qualification index (FQI) = 2.5 * [(10X +4Y)/RF] where
- X=No. of faculty members with Ph.D. degree or equivalent as per AICTE/UGC norms.
- Y=No. of faculty members with M. Tech. or ME degree or equivalent as per AICTE/ UGC norms.
- RF=No. of required faculty in the Department including allied Departments to adhere to the 20:1 Student-Faculty ratio, with calculations based on both student numbers and faculty requirements as per section C2 of this documents: (RF=S/20).

Table No.C3.1: Faculty qualification.

Year	X	Y	RF	FQ = 2.5 x [(10X + 4Y) / RF)]
2025-26(CAY)	7	18	23.00	15.43
2024-25(CAYm1)	8	20	22.00	18.18
2023-24(CAYm2)	7	19	21.00	17.38

C4. Faculty Cadre Proportion

- Faculty Cadre Proportion is 1(RF1): 2(RF2): 6(RF3)
- RF1= No. of Professors required = 1/9 * No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per C2 of this documents:.
- RF2= No. of Associate Professors required = 2/9 * No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents:.
- RF3= No. of Assistant Professors required = 6/9 * No. of Faculty required to comply with 20:1 Student-Faculty ratio based on no. of students (S) as per section C2 of this documents:.
- Faculty cadre and qualification and experience should be as per AICTE/UGC norms.

Table No.C4.1: Faculty cadre proportion details.

Year	Professors		Associate Professors		Assistant Professors	
	Required RF1	Available AF1	Required RF2	Available AF1	Required RF3	Available AF3
2025-26	2.00	2.00	5.00	3.00	15.00	20.00
2024-25	2.00	3.00	5.00	4.00	15.00	21.00
2023-24	2.00	3.00	4.00	3.00	14.00	20.00
Average	RF1=2.00	AF1=2.67	RF2=4.67	AF2=3.33	RF2=14.67	AF2=20.33

C5. Visiting/Adjunct Faculty/Professor of Practice

Table No. C5.1: List of visiting/adjunct faculty/professor of practice and their teaching and practical loads.

(CAYm1)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	P Venkata Sai Akhil	Senior Engineer	Smart Socs	Mixed Signal VLSI Design	30.00
2	A Balanna	Senior Physical Desing Engineer	EinfoChips	CMOS Digital IC Design	30.00
3	B Adinarayana	Senior Engineer	Common Services and Network Solutions, ARi, Hyderabad	Embedded System Design	30.00
4	B Adinarayana	Senior Engineer	Common Services and Network Solutions, ARi, Hyderabad	Internet of Things	30.00
5	A Balanna	Design Engineer	EinfoChips	Physical Design Automation	30.00
6	P Venkata Sai Akhil	Senior Engineer	Smart Socs	Low Power VLSI	30.00

(CAYm2)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	A Balanna	Senior Physical Desing Engineer	EinfoChips	VLSI Design	30.00
2	B Adinarayana	Senior Engineer	Common Services and Network Solutions, ARi, Hyderabad	Internet of Things	30.00
3	B Adinarayana	Senior Engineer	Common Services and Network Solutions, ARi, Hyderabad	Embedded System Design	30.00
4	A Balanna	Senior Physical Desing Engineer	EinfoChips	Physical Design Automation	30.00

(CAYm3)

S.No	Name of the Person	Designation	Organization	Name of the Course	No. of hours handled
1	B Adinarayana	Senior Engineer	Common Services and Network Solutions, ARi, Hyderabad	Embedded System Design,	30.00
2	B Adinarayana	Senior Engineer	Common Services and Network Solutions, ARi, Hyderabad	Internet of Things	30.00
3	A Balanna	Senior Physical Desing Engineer	EinfoChips	VLSI Design	30.00
4	A Balanna	Senior Physical Desing Engineer	EinfoChips	Physical Design Automation	30.00

C6. Academic Research

Table No. C6.1: Faculty publication details.

S.No.	Item	2024-25 (CAYm1)	2023-24 (CAYm2)	2022-23 (CAYm3)
1	No. of peer reviewed journal papers published	16	11	7
2	No. of peer reviewed conference papers published	23	8	12
3	No. of books/book chapters published	1	2	1

C7. Sponsored Research Project

Table No. C7.1: List of sponsored research projects received from external agencies.

(CAYm1)

(CAYm2)

(CAYm3)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr M V Subramanyam	Dr Y Mallikarjuna Rao	Electronics and Communication ENgineering	Design and Performance Analysis of Content Addressable Memory Cells and its Associative Circuits using CNTFETS for Next generation Networks	AICTE	3 Years	15.30
						Amount received (Rs.):15.30

Total Amount (Lacs) Received for the Past 3 Years: 15.30

Note*:

- Only sponsored research projects will be considered. Infrastructure-based projects will not be considered here.

C8. Consultancy Work

Table No. C8.1: List of consultancy projects received from external agencies.

(CAYm1)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr G Sowmya	Dr M V Subramanyam	Eleectronics and Communication Engineering	Position, Navigation and Timing Analysis ov various projects	IIT Tirupati Navavishkar I-Hub Foundation	1 Year	4.00
						Amount received (Rs.):4.00

(CAYm2)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr Y Mallikarjuna Rao	Dr M V Subramanyam	Eleectronics and Communication Engineering	Design and Development of Home Automation Systems	Novel Innovative Electronica Pvt Ltd	1 Year	5.00
						Amount received (Rs.):5.00

(CAYm3)

PI Name	Co-PI names if any	Name of the Dept., where project is sanctioned	Project Title*	Name of the Funding agency	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25
Dr Y Mallikarjuna Rao	Dr G Sowmya	Eleectronics and Communication Engineering	Design and Development of Smart Attendance System, Alcohol and Disese Detection Systems	Novel Innovative Electronica Pvt Ltd	1 Year	6.65
						Amount received (Rs.):6.65

Total amount (Lacs) received for the past 3 years: 15.65

Note*:

- Only consultancy projects will be considered. Infrastructure-based projects will not be considered here.

C9. Institution Seed Money or Internal Research Grant to its Faculty for Research Work

Table No. C9.1: List of faculty members received seed money or internal research grant from the Institution.

(CAYm1)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Mr. JAVID BASHA SHAIK	VLSI IMPLEMENTATION OF A BARREL SHIFTER	6 MONTHS	0.21	0.19	PROJECT & PUBLICATION
Mr. JAVID BASHA SHAIK	STUDY OF DYNAMIC COMPARATORS ON THE BASIS OF ENERGY CONSUMPTION	6 MONTHS	0.23	0.20	PROJECT & PUBLICATION
Dr KATEPOGU RAJKUMAR	BINARY TO GRAY CONVERTE	6 MONTHS	0.23	0.22	PROJECT & PUBLICATION
GATIKAPOGU NEELIMA	SELF-REPAIRING HYBRID ADDER WITH HOT-STANDYBY TOPOLOGY USING FAULT-LOCALIZATION	6 MONTHS	0.24	0.21	PROJECT & PUBLICATION
Dr KATEPOGU RAJKUMAR	AN OVERVIEW OF DYNAMIC CMOS COMPARATORS	6 MONTHS	0.25	0.21	PROJECT & PUBLICATION
Dr KATEPOGU RAJKUMAR	RCA-CSA ADDER BASED VEDIC MULTIPLIER	6 MONTHS	0.22	0.18	PROJECT & PUBLICATION
Mr. SANIVARAPU RAMBABU	4-BIT VEDIC MULTIPLIER WITH 18nm FINFET TECHNOLOGY	6 MONTHS	0.25	0.21	PROJECT & PUBLICATION
Mr. JAVID BASHA SHAIK	DESIGN OF MULTIPLEXER IN MULTIPLE LOGIC STYLES FOR LOW POWER VLSI	6 MONTHS	0.21	0.23	PROJECT & PUBLICATION
Mr. JAVID BASHA SHAIK	ALL OPTICAL DESIGN OF HYBRID ADDER CIRCUIT USING TERAHERTZ OPTICAL ASYMMETRIC DEMULTIPLEXER	6 MONTHS	0.26	0.22	PROJECT & PUBLICATION
Mr. SANKARNATH VEMUGADDA	Hybrid MobileNet-LSTM Model for Deepfake Detection	6 MONTHS	0.24	0.20	PROJECT & PUBLICATION
Dr. GALI SOWMYA	A Fake Website Detection using Machine learning	6 MONTHS	0.24	0.20	PROJECT & PUBLICATION
Mr. JAVID BASHA SHAIK	CMOS VLSI DESIGN OF A PULSED DUAL-EDGE FLIP-FLOP	6 MONTHS	0.22	0.19	PROJECT & PUBLICATION
Mr. SANIVARAPU RAMBABU	LOW-POWER, HIGH-SPEED PTL-BASED MULTIPLIER DESIGN	6 MONTHS	0.23	0.19	PROJECT & PUBLICATION
Dr. GALI SOWMYA	DESIGN OF CARRY SELECT ADDER WITH ONLINE TESTABILITY USING REVERSIBLE GATES	6 MONTHS	0.24	0.20	PROJECT & PUBLICATION
Mrs. NAGAMANI VADALA	DESIGN OF TERNARY LOGIC AND ARITHEMATIC CIRCUITS USING GNRFET	6 MONTHS	0.25	0.21	PROJECT & PUBLICATION
Dr. YAMARTHY MALLIKARJUNA RAO	RADIX-4 BOOTH COMPLEX MULTIPLIER USING CYCLIC REDUNDANT ADDER	6 MONTHS	0.25	0.21	PROJECT & PUBLICATION
Dr NAYAKANTI PRAVEEN KUMAR	OPTIMIZING TERNARY MULTIPLIER DESIGN WITH FAST TERNARY ADDER	6 MONTHS	0.23	0.20	PROJECT & PUBLICATION
Dr. VENKATA SUBRAMANYAM MAKAM	CNTFET BASED MULTIPLEXERS AND MAGNITUDE COMPARATOR	6 MONTHS	0.23	0.20	PROJECT & PUBLICATION
Mr. JAVID BASHA SHAIK	MGDI FULL ADDER USING POWER GATING TECHNIQUE	6 MONTHS	0.23	0.19	PROJECT & PUBLICATION

			Amount received (Rs.): 4.46		

(CAYm2)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Mr. SANIVARAPU RAMBABU	CNTFET-TA-FA	6 MONTHS	0.31	0.27	PROJECT & PUBLICATION
Mrs. BIKKILI ALEKYA HIMABINDU	DeepLearning with based animal detection with SMS & IOT Notification	6 MONTHS	0.25	0.21	PROJECT & PUBLICATION
Mr. MUSARAKALLU MAHESH KUMAR	Li-Fi Based underwater communication	6 MONTHS	0.35	0.29	PROJECT & PUBLICATION
Dr. VENKATA SUBRAMANYAM MAKAM	Parking management system	6 MONTHS	0.34	0.29	PROJECT & PUBLICATION
Dr. YAMARTHY MALLIKARJUNA RAO	Determine the practice and study of hiding information using Cryptography	6 MONTHS	0.30	0.25	PROJECT & PUBLICATION
Dr. GALI SOWMYA	Auto service Robot for catering Business canteen using Aurdino mega 2560	6 MONTHS	0.33	0.28	PROJECT & PUBLICATION
Dr. S. N. CHAUDHRI	Automated detection for Air Pollution in Vehicles by using Raspberry pi & GPS	6 MONTHS	0.32	0.27	PROJECT & PUBLICATION
Mr. MUNAWWAR SYED	Enhancing Medical image fusion with Deep Learning	6 MONTHS	0.33	0.28	PROJECT & PUBLICATION
Mrs. NAGAMANI VADALA	Flood Monitoring & Alerting System using Aurdino IOT	6 MONTHS	0.33	0.28	PROJECT & PUBLICATION
Mr. NOVULE RAO SREENIVASARAO	Design & Development of vehicle safety detection and alarm system	6 MONTHS	0.32	0.27	PROJECT & PUBLICATION
Mr. JAVID BASHA SHAIK	Design of Quaternary Logic gates & Arithmetic circuits using GNRFETs	6 MONTHS	0.31	0.26	PROJECT & PUBLICATION
Mrs. NAGAMANI VADALA	Sentiment Analysis model using Machine Learning Algorithm	6 MONTHS	0.30	0.28	PROJECT & PUBLICATION
			Amount received (Rs.): 3.79		

(CAYm3)

Faculty name	Project title/ Support for Activity	Duration of the project	Amount(Lacs) i.e. 15,25,000=15.25	Amount Utilized(Lacs) i.e. 15,25,000=15.25	Outcomes of the project
Dr. VENKATA SUBRAMANYAM MAKAM	IoT based farm monitoring and protection using ESP32 camera module	6 MONTHS	0.39	0.33	PROJECT & PUBLICATION
Dr. YAMARTHY MALLIKARJUNA RAO	Auto Railway Platform Control using sensors	6 MONTHS	0.35	0.29	PROJECT & PUBLICATION
Dr. GALI SOWMYA	Automatic Agricultural Robot – Agrobot	6 MONTHS	0.34	0.29	PROJECT & PUBLICATION
Mr. NOVULE RAO SREENIVASARAO	IOT based wireless sensor network for air pollution monitoring	6 MONTHS	0.33	0.28	PROJECT & PUBLICATION
Mr. MUNAWWAR SYED	Embedded IOT Car Parking And Billing System	6 MONTHS	0.36	0.30	PROJECT & PUBLICATION
Mrs. NAGAMANI VADALA	IOT vehicle tracking and rfid access system	6 MONTHS	0.28	0.24	PROJECT & PUBLICATION
Mrs. BIKKILI ALEKYA HIMABINDU	Smart Agriculture monitoring and management system for efficient crop growth.	6 MONTHS	0.30	0.25	PROJECT & PUBLICATION
Mr. MUSARAKALLU MAHESH KUMAR	IoT-Based Traffic Control with Auto-Fine and Pollution Detection	6 MONTHS	0.32	0.27	PROJECT & PUBLICATION
			Amount received (Rs.): 2.67		

Total amount (Lacs) received for the past 3 years : 10.92

PART D: Laboratory Infrastructure in the Department

(Data to be filled in for the Department)

D1. Adequate and Well-Equipped Laboratories, and Technical Manpower

Table No.D1.1: List of laboratories and technical manpower.

Sr. No	Name of the Laboratory	Number of students per set up(Batch Size)	Name of the Important Equipment	Weekly utilization status(all the courses for which the lab is utilized)	Technical Manpower Support		
					Name of the Technical staff	Designation	Qualification
1	Electronic Devices & Circuits LAB	3	Cathode Ray Oscilloscopes, Digital Storage Oscilloscopes, Function Generators, Regulated Power Supplies, EDC experiment kits	30	Mr. N.V.S. Prasad	Lab Technician	B.Tech
2	Microprocessors & Interfacing Lab	3	DLD Kits & 3x8 Decoder, Synchronous & Asynchronous Trainer Kit, D-Flip-flop, R-S Flip-Flop kit, Ring Counter 8x1 Multiplexer & De-multiplexer, MOD-8 asynchronous	24	Mr. P. Raghu Ramaiah	Lab Technician	M.Sc (Electronics)
3	VLSI & Embedded Systems Lab	1	Computers & Python software, Kicad 9.0.7 Open Source software, Tinkercad, Anakonda Environment Jupyter Notebook 7, MATLAB R2019A 9.6.0, Cadence	30	Mrs. B. Mounika	Lab Technician	B.Tech
4	IC Applications Lab	3	ASLK Pro Kits, Function Generators, Cathode Ray Oscilloscopes, Digital Storage Oscilloscopes	12	Mr. S. Amar Shan	Lab Technician	Diploma
5	Basic Simulation Lab	1	Computers & Python software, Kicad 9.0.7 Open Source software, Tinkercad, Anakonda Environment Jupyter Notebook 7, MATLAB R2019A 9.6.0	30	Mr. R. Siva Kumar	Lab Technician	M.Sc (Electronics)
6	ADC & MW-OC LAB	3	Cathode Ray Oscilloscopes, Analog Communication Trainer Kits, DC Trainer Kits, Function Generators, Digital Storage Oscilloscopes, Regulated	24	Mr. C. Vamsi Krishna	Lab Technician	Diploma

D2. Safety Measures in Laboratories

Table No. D2.1: List of various safety measures in laboratories.

Sr. No	Laboratory Name	Safety Measures
1	Electronic Devices & Circuits LAB	1) Laboratory rules and SOPs are clearly displayed in all labs. 2) Regular servicing and preventive maintenance of equipment are carried out. 3) Laboratories are maintained clean, safe, and well-organized. 4) Fire extinguishers and first-aid boxes are available and periodically inspected. 5) Mobile phone usage inside laboratories is restricted. 6) Students verify equipment condition before use and report faults immediately. 7) Personal belongings are kept in designated storage areas. 8) Safety instructions are prominently displayed in laboratories. 9) Trained technical staff supervises laboratory operations continuously. 10) Students follow prescribed dress code during lab sessions. 11) Fire safety equipment is checked periodically. 12) Proper electrical wiring and grounding are ensured.
2	Microprocessors & Interfacing Lab	□ Laboratory rules and SOPs are clearly displayed in all labs. □ Regular servicing and preventive maintenance of equipment are carried out. □ Laboratories are maintained clean, safe, and well-organized. □ Fire extinguishers and first-aid boxes are available and periodically inspected. □ Mobile phone usage inside laboratories is restricted. □ Students verify equipment condition before use and report faults immediately. □ Personal belongings are kept in designated storage areas. □ Safety instructions are prominently displayed in laboratories. □ Trained technical staff supervises laboratory operations continuously. □ Students follow prescribed dress code during lab sessions. □ Fire safety equipment is checked periodically. □ Proper electrical wiring and grounding are ensured.

3	VLSI & Embedded Systems Lab	□ Laboratory rules and SOPs are clearly displayed in all labs. □ Laboratories are maintained clean, safe, and well-organized. □ Fire extinguishers and first-aid boxes are available and periodically inspected. □ CCTV surveillance is provided in all laboratories for safety and monitoring. □ Mobile phone usage inside laboratories is restricted. □ Use of pen drives/external devices is restricted for data security. □ Systems are properly shut down after laboratory sessions. □ Footwear removal is mandated in designated laboratories. □ Students verify system condition before use and report faults immediately. □ Personal belongings are kept in designated storage areas. □ Safety instructions are prominently displayed in laboratories. □ Trained technical staff supervises laboratory operations continuously. □ Air-conditioning is provided in all laboratories. □ Students follow prescribed dress code during lab sessions. □ Fire safety equipment is checked periodically. □ Proper electrical wiring and grounding are ensured. □ Antivirus and security software are installed on lab systems. □ UPS backup is provided for uninterrupted laboratory operation. □ Sensor-based safety systems are installed for enhanced safety monitoring.
4	IC Applications Lab	□ Laboratory rules and SOPs are clearly displayed in all labs. □ Regular servicing and preventive maintenance of equipment are carried out. □ Laboratories are maintained clean, and well-organized. □ Fire extinguishers and first-aid boxes are available and periodically inspected. □ Mobile phone usage inside laboratories is restricted. □ Students verify equipment condition before use and report faults immediately. □ Personal belongings are kept in designated storage areas. □ Safety instructions are prominently displayed in laboratories. □ Trained technical staff supervises laboratory operations continuously. □ Students follow prescribed dress code during lab sessions. □ Fire safety equipment is checked periodically. □ Proper electrical wiring and grounding are ensured.
5	Basic Simulation Lab	□ Laboratory rules and SOPs are clearly displayed in all labs. □ Laboratories are maintained clean, safe, and well-organized. □ Fire extinguishers and first-aid boxes are available and periodically inspected. □ CCTV surveillance is provided in all laboratories for safety and monitoring. □ Mobile phone usage inside laboratories is restricted. □ Use of pen drives/external devices is restricted for data security. □ Systems are properly shut down after laboratory sessions. □ Footwear removal is mandated in designated laboratories. □ Students verify system condition before use and report faults immediately. □ Personal belongings are kept in designated storage areas. □ Safety instructions are prominently displayed in laboratories. □ Trained technical staff supervises laboratory operations continuously. □ Air-conditioning is provided in all laboratories. □ Students follow prescribed dress code during lab sessions. □ Fire safety equipment is checked periodically. □ Proper electrical wiring and grounding are ensured. □ Antivirus and security software are installed on lab systems. □ UPS backup is provided for uninterrupted laboratory operation. □ Sensor-based safety systems are installed for enhanced safety monitoring.
6	ADC & MW-OC LAB	□ Laboratory rules and SOPs are clearly displayed in all labs. □ Regular servicing and preventive maintenance of equipment are carried out. □ Laboratories are maintained clean, safe, and well-organized. □ Fire extinguishers and first-aid boxes are available and periodically inspected. □ Mobile phone usage inside laboratories is restricted. □ Students verify equipment condition before use and report faults immediately. □ Personal belongings are kept in designated storage areas. □ Safety instructions are prominently displayed in laboratories. □ Trained technical staff supervises laboratory operations continuously. □ Students follow prescribed dress code during lab sessions. □ Fire safety equipment is checked periodically. □ Proper electrical wiring and grounding are ensured.

D3. Project Laboratory/Research Laboratory

PART E: First Year faculty and financial Resources

(Data to be filled in for the first year course faculty and budget allocation and utilization)

E1. First Year Student-Faculty Ratio (FYSFR)

Table No. E1.1: FYSFR details.

Year	Sanctioned intake of all UG programs (S4)	No. of required faculty (RF4= S4/20)	No. of faculty members in Basic Science Courses & Humanities and Social Sciences including Management courses (NS1)	No. of faculty members in Engineering Science Courses (NS2)	Percentage= No. of faculty members ((NS1*0.8) + (NS2*0.2))/(No. of required faculty (RF4)); Percentage= ((NS1*0.8) +(NS2*0.2))/RF
2023-24(CAYm2)	420	21	24	6	97
2024-25(CAYm1)	630	32	34	8	90

2025-26(CAY)	870	44	46	12	89

E2. Budget Allocation, Utilization, and Public Accounting at Institute Level

Table No. E2.1: Budget and actual expenditure incurred at Institute level.

Items	Budgeted in 2025-26	Actual Expenses in 2025-26 till	Budgeted in 2024-25	Actual Expenses in 2024-25 till	Budgeted in 2023-24	Actual Expenses in 2023-24 till	Budgeted in 2022-23	Actual Expenses in 2022-23 till
Infrastructure Built-Up	23000000	22297749	21000000	20754784	7000000	6812273	7400000	7085490
Library	150000	136974	400000	366573	695000	689539	500000	475955
Laboratory equipment	6500000	6474979	8000000	7889925	5300000	5146449	4500000	4387345
Teaching and non-teaching staff salary	107500000	102510086	95000000	93190987	73000000	71991500	66000000	63293239
Outreach Programs	900000	825288	1750000	1657469	1100000	1065840	1525000	1497684
R&D	2100000	2004560	2300000	2212290	1950000	1929280	1890000	1862000
Training, Placement and Industry linkage	2200000	2136677	2300000	2214804	2000000	1957633	1920000	1884486
SDGs	350000	330342	800000	714978	1700000	1625352	6350000	6129582
Entrepreneurship	700000	681298	650000	613362	1500000	1471396	360000	355814
Software, Support for Faculty Development, Miscellaneous	12520000	12267345	10650000	9320609	20400000	18794920	15540000	14831229
Total	**155920000**	**149665298**	**142850000**	**138935781**	**114645000**	**111484182**	**105985000**	**101802824**

E3. Budget Allocation, Utilization, and Public Accounting at Program Specific Level

Table No. E3.1: Budget and actual expenditure incurred at program level.

Items	Budgeted in 2025-26	Actual Expenses in 2025-26 till	Budgeted in 2024-25	Actual Expenses in 2024-25 till	Budgeted in 2023-24	Actual Expenses in 2023-24 till	Budgeted in 2022-23	Actual Expenses in 2022-23 till
Laboratory equipment	1060000	1046626	550000	512164	1400000	1335018	1600000	1562354
Software	150000	130119	60000	52767	310000	309942	1200000	1150500
SDGs	75000	63801	150000	144426	410000	403012	200000	193569
Support for faculty development	400000	364650	175000	150640	200000	183440	120000	112960

R & D	425000	353636	400000	396884	400000	379190	300000	266291
Industrial Training, Industry expert, Internship	425000	412671	450000	447392	400000	385402	400000	382161
Miscellaneous Expenses*	600000	592088	575000	560969	600000	557136	250000	220168
Total	**3135000**	**2963591**	**2360000**	**2265242**	**3720000**	**3553140**	**4070000**	**3888003**